IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTORS: Esko Nieminen
SERIAL NO.: 10/696,926   ART UNIT:
FILED: 10/30/2003   EXAMINER:
TITLE: Noise And Quality Detector For Use With Turbo Coded Signals
ATTORNEY DOCKET NO.: 872.0154.U1(US)

Commissioner For Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,

_____
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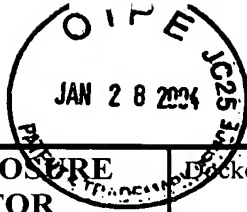Facsimile: (203)944-0245
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date   Name of Person Making Deposit

INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)	Docket No.: 872.0154.U1(US)	Serial No.: 10/696,926
	Applicant(s): Esko Nieminen	
	Filing Date: 10/30/03	Group:

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
	US-				
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
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OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

	"Real-Time Algorithms and VLSI Architectures for Soft Output Map Convolutional Decoding",, Dawid, H., et al., in 6th IEEE International Symposium on Personal, Indoor and Mobile Radio Communications, Toronto, Canada, Volume 1, pp. 193-197, September 1995
	"Memory Efficient Implementation of the BCJR Algorithm, in Proceedings of the 2nd International Symposium on Turbo Codes and Related Topics", Huettinger, S. et al., Brest, France, 4-7 September 2000, pp. 479-482

Examiner's Signature:	Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
Include a copy of this citation form with your next correspondence to the Applicant(s).